Lecture 24

Circuit Theory Revisited

24.1 Circuit Theory Revisited

Circuit theory is one of the most successful and often used theories in electrical engineering. Its success is mainly due to its simplicity: it can capture the physics of highly complex circuits and structures, which is very important in the computer and micro-chip industry. Now, having understood electromagnetic theory in its full glory, it is prudent to revisit circuit theory and study its relationship to electromagnetic theory [29, 31, 48, 59].

The two most important laws in circuit theory are Kirchoff current law (KCL) and Kirchhoff voltage law (KVL) [14, 45]. These two laws are derivable from the current continuity equation and from Faraday's law.

24.1.1 Kirchhoff Current Law

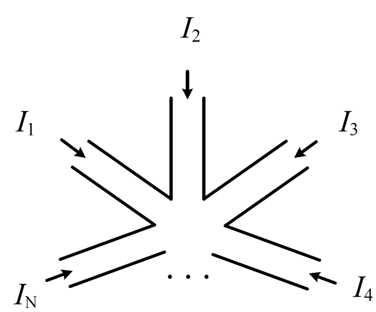

Figure 24.1: Schematics showing the derivation of Kirchhoff current law. All currents flowing into a node must add up to zero.

Kirchhoff current law (KCL) is a consequence of the current continuity equation, or that

$$\nabla \cdot \mathbf{J} = -j\omega\varrho \tag{24.1.1}$$

It is a consequence of charge conservation. But it is also derivable from generalized Ampere's law and Gauss' law for charge.[1]

First, we assume that all currents are flowing into a node as shown in Figure 24.1, and that the node is non-charge accumulating with $\omega \to 0$. Then the charge continuity equation becomes

$$\nabla \cdot \mathbf{J} = 0 \tag{24.1.2}$$

By integrating the above current continuity equation over a volume containing the node, it is easy to show that

$$\sum_i^N I_i = 0 \tag{24.1.3}$$

which is the statement of KCL. This is shown for the schematics of Figure 24.1.

24.1.2 Kirchhoff Voltage Law

Kirchhoff voltage law is the consequence of Faraday's law. For the truly static case when $\omega = 0$, it is

$$\nabla \times \mathbf{E} = 0 \tag{24.1.4}$$

The above implies that $\mathbf{E} = -\nabla\Phi$, from which we can deduce that

$$-\oint_C \mathbf{E} \cdot d\mathbf{l} = 0 \tag{24.1.5}$$

For statics, the statement that $\mathbf{E} = -\nabla\Phi$ also implies that we can define a voltage drop between two points, a and b to be

$$V_{ba} = -\int_a^b \mathbf{E} \cdot d\mathbf{l} = \int_a^b \nabla\Phi \cdot d\mathbf{l} = \Phi(\mathbf{r}_b) - \Phi(\mathbf{r}_a) = V_b - V_a \tag{24.1.6}$$

As has been shown before, to be exact, $\mathbf{E} = -\nabla\Phi - \partial/\partial t\mathbf{A}$, but we have ignored the induction effect. Therefore, this concept is only valid in the low frequency or long wavelength limit, or that the dimension over which the above is applied is very small so that retardation effect can be ignored.

A good way to remember the above formula is that if $V_b > V_a$, then the electric field points from point a to point b. Electric field always points from the point of higher potential

[1]Some authors will say that charge conservation is more fundamental, and that Gauss' law and Ampere's law are consistent with charge conservation and the current continuity equation.

to point of lower potential. Faraday's law when applied to the static case for a closed loop of resistors shown in Figure 24.2 gives Kirchhoff voltage law (KVL), or that

$$\sum_{i}^{N} V_j = 0 \tag{24.1.7}$$

Notice that the voltage drop across a resistor is always positive, since the voltages to the left of the resistors in Figure 24.2 are always higher than the voltages to the right of the resistors. This implies that internal to the resistor, there is always an electric field that points from the left to the right.

If one of the voltage drops is due to a voltage source, it can be modeled by a negative resistor as shown in Figure 24.3. The voltage drop across a negative resistor is opposite to that of a positive resistor. As we have learn from the Poynting's theorem, negative resistor gives out energy instead of dissipates energy.

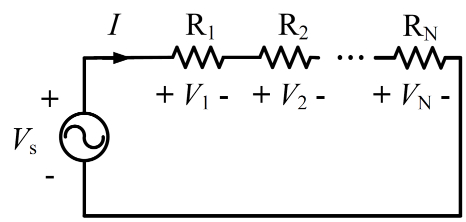

Figure 24.2: Kichhoff voltage law where the sum of all voltages around a loop is zero, which is the consequence of static Faraday's law.

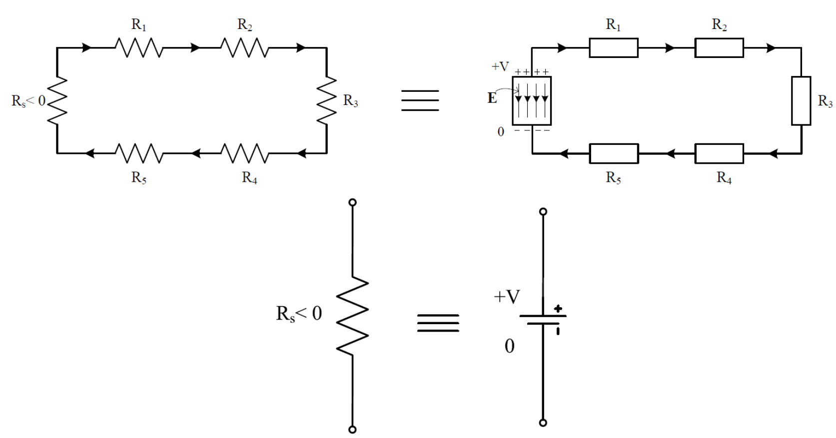

Figure 24.3: A voltage source can also be modeled by a negative resistor.

Faraday's law for the time-varying case is

$$\nabla \times \mathbf{E} = -\frac{\partial \mathbf{B}}{\partial t} \tag{24.1.8}$$

Writing the above in integral form, one gets

$$-\oint_C \mathbf{E} \cdot \mathbf{dl} = \frac{d}{dt} \int_s \mathbf{B} \cdot \mathbf{dS} \tag{24.1.9}$$

We can apply the above to a loop shown in Figure 24.4, or a loop C that goes from a to b to c to d to a. We can further assume that this loop is very small compared to wavelength so that potential theory that $\mathbf{E} = -\nabla\Phi$ can be applied. Furthermore, we assume that this loop C does not have any magnetic flux through it so that the right-hand side of the above can be set to zero, or

$$-\oint_C \mathbf{E} \cdot \mathbf{dl} = 0 \tag{24.1.10}$$

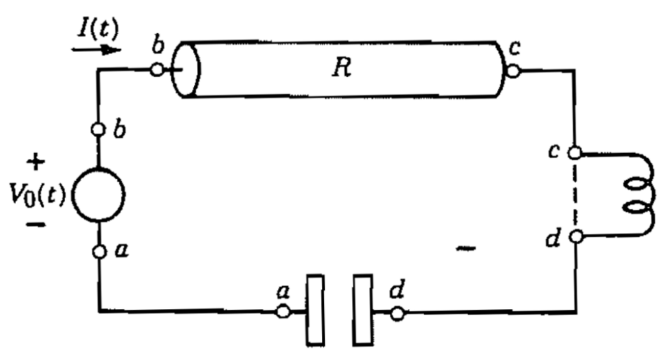

Figure 24.4: The Kirchhoff voltage law for a circuit loop consisting of resistor, inductor, and capacitor can also be derived from Faraday's law at low frequency.

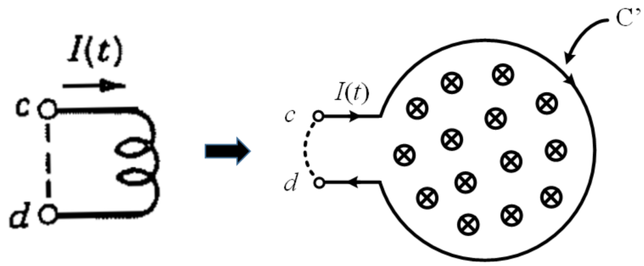

Figure 24.5: The voltage-current relation of an inductor can be obtained by unwrapping an inductor coil, and then calculate its flux linkage.

Notice that this loop does not go through the inductor, but goes directly from c to d. Then there is no flux linkage in this loop and thus

$$-\int_a^b \mathbf{E}\cdot\mathbf{dl} - \int_b^c \mathbf{E}\cdot\mathbf{dl} - \int_c^d \mathbf{E}\cdot\mathbf{dl} - \int_d^a \mathbf{E}\cdot\mathbf{dl} = 0 \tag{24.1.11}$$

Inside the source or the battery, it is assumed that the electric field points opposite to the direction of integration **dl**, and hence the first term on the left-hand side of the above is positive while the other terms are negative. Writing out the above more explicitly, we have

$$V_0(t) + V_{cb} + V_{dc} + V_{ad} = 0 \tag{24.1.12}$$

Notice that in the above, in accordance to (24.1.6), $V_b > V_c$, $V_c > V_d$, and $V_a > V_a$. Therefore, V_{cb}, V_{dc}, and V_{ad} are all negative quantities but $V_0(t) > 0$. We will study the contributions to each of the terms, the inductor, the capacitor, and the resistor more carefully next.

24.1.3 Inductor

To find the voltage current relation of an inductor, we apply Faraday's law to a closed loop C' formed by dc and the inductor coil shown in the Figure 24.5 where we have unwrapped the solenoid into a larger loop. Assume that the inductor is made of a very good conductor, so that the electric field in the wire is small or zero. Then the only contribution to the left-hand side of Faraday's law is the integration from point d to point c. We assume that outside the loop in the region between c and d, potential theory applies, and hence, $\mathbf{E} = -\nabla\Phi$. Now, we can connect V_{dc} in the previous equation to the flux linkage to the inductor. When the voltage source attempts to drive an electric current into the loop, Lenz's law (1834)[2] comes into effect, essentially, generating an opposing voltage. The opposing voltage gives rise to charge accumulation at d and c, and hence, a low frequency electric field at the gap.

To this end, we form a new C' that goes from d to c, and then continue onto the wire that leads to the inductor. But this new loop will contain the flux **B** generated by the inductor

[2]Lenz's law can also be explained from Faraday's law (1831).

current. Thus

$$\oint_{C'} \mathbf{E} \cdot \mathbf{dl} = \int_d^c \mathbf{E} \cdot \mathbf{dl} = -V_{dc} = -\frac{d}{dt} \int_{S'} \mathbf{B} \cdot \mathbf{dS} \tag{24.1.13}$$

The inductance L is defined as the flux linkage per unit current, or

$$L = \left[\int_{S'} \mathbf{B} \cdot \mathbf{dS} \right] / I \tag{24.1.14}$$

So the voltage in (24.1.13) is then

$$V_{dc} = \frac{d}{dt}(LI) = L\frac{dI}{dt} \tag{24.1.15}$$

Had there been a finite resistance in the wire of the inductor, then the electric field is non-zero inside the wire. Taking this into account, we have

$$\oint \mathbf{E} \cdot \mathbf{dl} = R_L I - V_{dc} = -\frac{d}{dt} \int_S \mathbf{B} \cdot \mathbf{dS} \tag{24.1.16}$$

Consequently,

$$V_{dc} = R_L I + L\frac{dI}{dt} \tag{24.1.17}$$

Thus, to account for the loss of the coil, we add a resistor in the equation. The above becomes simpler in the frequency domain, namely

$$V_{dc} = R_L I + j\omega L I \tag{24.1.18}$$

24.1.4 Capacitance

The capacitance is the proportionality constant between the charge Q stored in the capacitor, and the voltage V applied across the capacitor, or $Q = CV$. Then

$$C = \frac{Q}{V} \tag{24.1.19}$$

From the current continuity equation, one can easily show that in Figure 24.6,

$$I = \frac{dQ}{dt} = \frac{d}{dt}(CV_{da}) = C\frac{dV_{da}}{dt} \tag{24.1.20}$$

Integrating the above equation, one gets

$$V_{da}(t) = \frac{1}{C} \int_{-\infty}^{t} I dt' \tag{24.1.21}$$

The above looks quite cumbersome in the time domain, but in the frequency domain, it becomes

$$I = j\omega C V_{da} \tag{24.1.22}$$

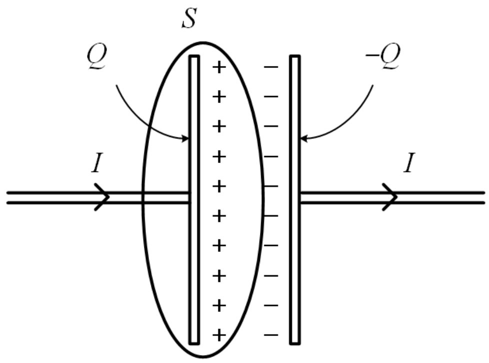

Figure 24.6: Schematics showing the calculation of the capacitance of a capacitor.

24.1.5 Resistor

The electric field is not zero inside the resistor as electric field is needed to push electrons through it. As is well known,

$$\mathbf{J} = \sigma \mathbf{E} \tag{24.1.23}$$

From this, we deduce that $V_{cb} = V_c - V_b$ is a negative number given by

$$V_{cb} = -\int_b^c \mathbf{E} \cdot \mathbf{dl} = -\int_b^c \frac{\mathbf{J}}{\sigma} \cdot \mathbf{dl} \tag{24.1.24}$$

where we assume a uniform current $\mathbf{J} = \hat{l} I/A$ in the resistor where $\hat{l}$ is a unit vector pointing in the direction of current flow in the resistor. We can assumed that I is a constant along the length of the resistor, and thus,

$$V_{cb} = -\int_b^c \frac{Idl}{\sigma A} = -I \int_b^c \frac{dl}{\sigma A} = -IR \tag{24.1.25}$$

and

$$R = \int_b^c \frac{dl}{\sigma A} \tag{24.1.26}$$

Again, for simplicity, we assume long wavelength or low frequency in the above derivation.

24.2 Some Remarks

In this course, we have learnt that given the sources ϱ and $\mathbf{J}$ of an electromagnetic system, one can find Φ and $\mathbf{A}$, from which we can find $\mathbf{E}$ and $\mathbf{H}$. This is even true at DC or statics. We have also looked at the definition of inductor L and capacitor C. But clever engineering is driven by heuristics: it is better, at times, to look at inductors and capacitors as energy storage devices, rather than flux linkage and charge storage devices.

Another important remark is that even though circuit theory is simpler that Maxwell's equations in its full glory, not all the physics is lost in it. The physics of the induction term in Faraday's law and the displacement current term in generalized Ampere's law are still retained. In fact, wave physics is still retained in circuit theory: one can make slow wave structure out a series of inductors and capacitors. The lumped-element model of a transmission line is an example of a slow-wave structure design. Since the wave is slow, it has a smaller wavelength, and resonators can be made smaller: We see this in the LC tank circuit which is a much smaller resonator in wavelength compared to a microwave cavity resonator for instance. The only short coming is that inductors and capacitors generally have higher losses than air or vacuum.

24.2.1 Energy Storage Method for Inductor and Capacitor

Often time, it is more expedient to think of inductors and capacitors as energy storage devices. This enables us to identify stray (also called parasitic) inductances and capacitances more easily. This manner of thinking allows for an alternative way of calculating inductances and capacitances as well [29].

The energy stored in an inductor is due to its energy storage in the magnetic field, and it is alternatively written, according to circuit theory, as

$$W_m = \frac{1}{2} L I^2 \tag{24.2.1}$$

Therefore, it is simpler to think that an inductance exists whenever there is stray magnetic field to store magnetic energy. A piece of wire carries a current that produces a magnetic field enabling energy storage in the magnetic field. Hence, a piece of wire in fact behaves like a small inductor, and it is non-negligible at high frequencies: Stray inductances occur whenever there are stray magnetic fields.

By the same token, a capacitor can be thought of as an electric energy storage device rather than a charge storage device. The energy stored in a capacitor, from circuit theory, is

$$W_e = \frac{1}{2} C V^2 \tag{24.2.2}$$

Therefore, whenever stray electric field exists, one can think of stray capacitances as we have seen in the case of fringing field capacitances in a microstrip line.

24.2.2 Finding Closed-Form Formulas for Inductance and Capacitance

Finding closed form solutions for inductors and capacitors is a difficult endeavor. Only certain geometries are amenable to closed form solutions. Even a simple circular loop does not have a closed form solution for its inductance L. If we assume a uniform current on a circular loop, in theory, the magnetic field can be calculated using Bio-Savart law that we have learnt before, namely that

$$\mathbf{H}(\mathbf{r}) = \int \frac{I(\mathbf{r}')\mathbf{dl}' \times \hat{R}}{4\pi R^2} \tag{24.2.3}$$

But the above cannot be evaluated in closed form save in terms of complicate elliptic integrals.

However, if we have a solenoid as shown in Figure 24.7, an approximate formula for the inductance L can be found if the fringing field at the end of the solenoid can be ignored. The inductance can be found using the flux linkage method [28, 29]. Figure 24.8 shows the schematics used to find the approximate inductance of this inductor.

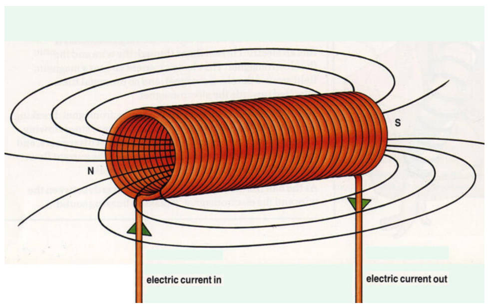

Figure 24.7: The flux-linkage method is used to estimate the inductor of a solenoid (courtesy of SolenoidSupplier.Com).

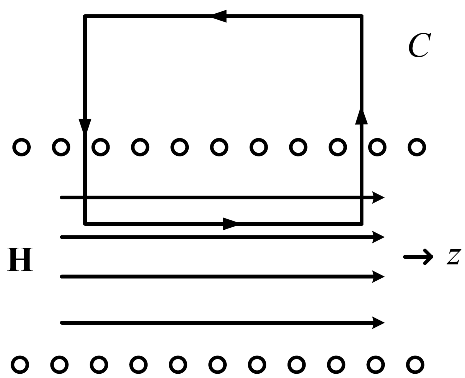

Figure 24.8: Finding the inductor flux linkage by assuming the magnetic field is uniform inside a long solenoid.

The capacitance of a parallel plate capacitor can be found by solving a boundary value problem (BVP) for electrostatics. The electrostatic BVP for capacitor involves Poisson's equation and Laplace equation which are scalar equations [42][Thomson's theorem].

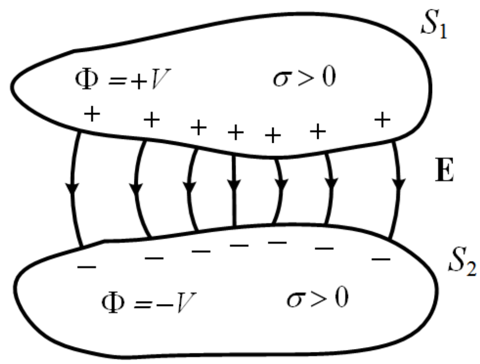

Figure 24.9: The capacitance between two charged conductors can be found by solving a boundary value problem (BVP).

Assume a geometry of two conductors charged to $+V$ and $-V$ volts as shown in Figure 24.9. Surface charges will accumulate on the surfaces of the conductors. Using Poisson's equations, and Green's function for Poisson's equation, one can express the potential in between the two conductors as due to the surface charges density $\sigma(\mathbf{r})$. It can be expressed as

$$\Phi(\mathbf{r}) = \frac{1}{\varepsilon} \int_S dS' \frac{\sigma(\mathbf{r}')}{4\pi|\mathbf{r} - \mathbf{r}'|} \tag{24.2.4}$$

where S is the union of two surfaces S_1 and S_2. Since Φ has values of $+V$ and $-V$ on the two conductors, we require that

$$\Phi(\mathbf{r}) = \frac{1}{\varepsilon} \int_S dS' \frac{\sigma(\mathbf{r}')}{4\pi|\mathbf{r} - \mathbf{r}'|} = \begin{cases} +V, & \mathbf{r} \in S_1 \\ -V, & \mathbf{r} \in S_2 \end{cases} \tag{24.2.5}$$

In the above, $\sigma(\mathbf{r}')$, the surface charge density, is the unknown yet to be sought and it is embedded in an integral. But the right-hand side of the equation is known. Hence, this equation is also known as an integral equation. The integral equation can be solved by numerical methods.

Having found $\sigma(\mathbf{r})$, then it can be integrated to find Q, the total charge on one of the conductors. Since the voltage difference between the two conductors is known, the capacitance can be found as $C = Q/(2V)$.

24.3 Importance of Circuit Theory in IC Design

The clock rate of computer circuits has peaked at about 3 GHz due to the resistive loss, or the I^2R loss. At this frequency, the wavelength is about 10 cm. Since transistors and circuit components are shrinking due to the compounding effect of Moore's law, most components, which are of nanometer dimensions, are much smaller than the wavelength. Thus, most of the physics of electromagnetic signal in a circuit can be captured using circuit theory.

Figure 24.10 shows the schematics and the cross section of a computer chip at different levels: the transistor level at the bottom-most. The signals are taken out of a transistor by XY lines at the middle level that are linked to the ball-grid array at the top-most level of the chip. And then, the signal leaves the chip via a package. Since these nanometer-size structures are much smaller than the wavelength, they are usually modeled by lumped R, L, and C elements if retardation effect can be ignored. If retardation effect is needed, it is usually modeled by a transmission line. This is important at the package level where the dimensions of the components are larger.

A process of parameter extraction where computer software or field solvers (software that solve Maxwell's equations numerically) are used to extract these lumped-element parameters. Finally, a computer chip is modeled as a network involving a large number of transistors, diodes, and R, L, and C elements. Subsequently, a very useful commercial software called SPICE (Simulation Program with Integrated-Circuit Emphasis) [123], which is a computer-aided software, solves for the voltages and currents in this network.

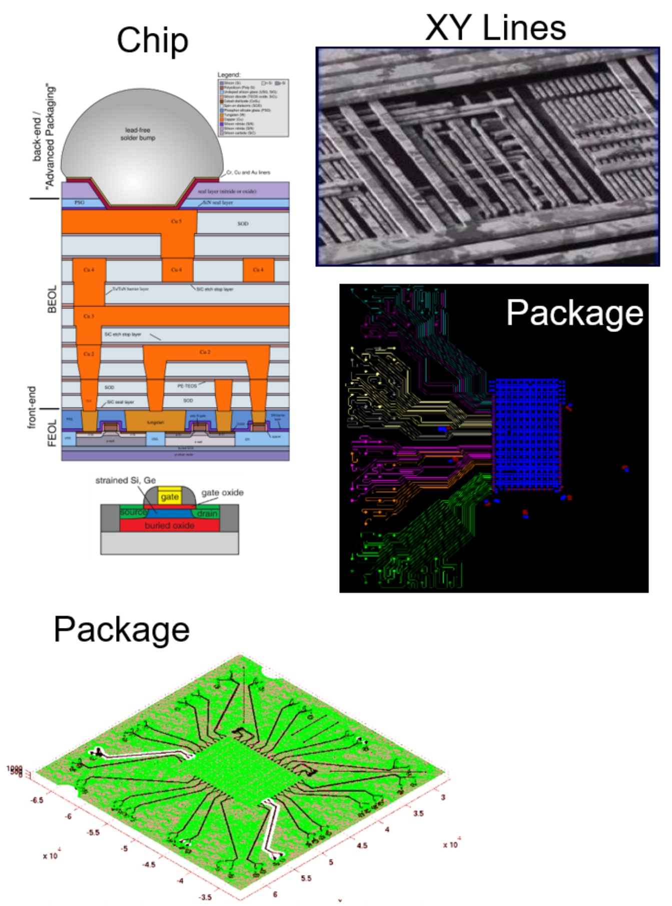

Figure 24.10: Courtesy of Wikipedia and Intel.

The SPICE software has many capabilities, including modeling of transmission lines for microwave engineering. Figure 24.11 shows an interface of an RF-SPICE that allows the modeling of transmission line with a Smith chart interface.

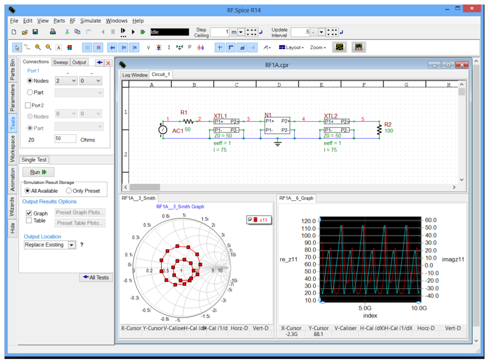

Figure 24.11: SPICE is also used to solve RF problems (courtesy of EMAG Technologies Inc.).

24.3.1 Decoupling Capacitors and Spiral Inductors

Decoupling capacitors are an important part of modern computer chip design. They can regulate voltage supply on the power delivery network of the chip as they can remove high-frequency noise and voltage fluctuation from a circuit as shown in Figure 24.12. Figure 24.13 shows a 3D IC computer chip where decoupling capacitors are integrated into its design.

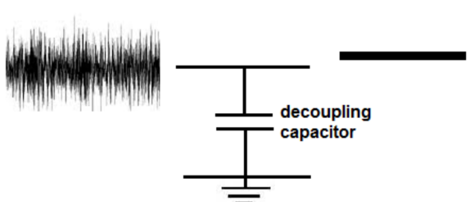

Figure 24.12: A decoupling capacitor is essentially a low-pass filter allowing low-frequency signal to pass through, while high-frequency signal is short-circuited (courtesy learningabout-electronics.com).

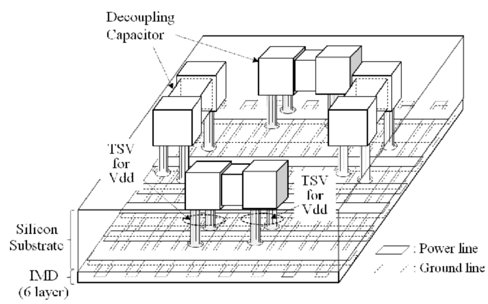

Figure 24.13: Modern computer chip design is 3D and is like a jungle. There are different levels in the chip and they are connected by through silicon vias (TSV). IMD stands for inter-metal dielectrics. One can see different XY lines serving as power and ground lines (courtesy of Semantic Scholars).

Inductors are also indispensable in IC design, as they can be used as a high frequency choke. However, designing compact inductor is still a challenge. Spiral inductors are used because of their planar structure and ease of fabrication.

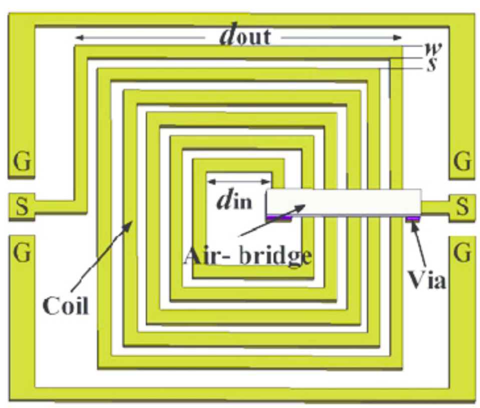

Figure 24.14: Spiral inductors are difficult to build on a chip, but by using laminal structure, it can be integrated into the IC fabrication process (courtesy of Quan Yuan, Research Gate).

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26 décembre 1820, 10 juin 1822, 22 décembre 1823, 12 septembre et 21 novembre 1825. Bachelier, 1825.

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